

103103

16569

U.S. PTO

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**CERTIFICATE OF EXPRESS MAILING**

I hereby certify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service on October 31, 2003 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number EV332824069US, addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Ryan Eachus

Attorney Docket No.: 02-6352/LSI1P220

First Named Inventor: Mohammad R.

Mirabedini

19270 U.S. PTO
10/69816710/698167
103103**UTILITY PATENT APPLICATION TRANSMITTAL (37 CFR. § 1.53(b))**

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir: This is a request for filing a patent application under 37 CFR. § 1.53(b) in the name of inventors:
Mohammad R. Mirabedini, Grace S. Sun, and Sheldon Aronowitz

For: **CALCIUM DOPED POLYSILICON GATE ELECTRODES**

Assigned to: LSI Logic Corporation

Application Elements:

- 12** Pages of Specification, Claims and Abstract
- 08** Sheets of informal Drawings
- 06** Pages Combined Declaration and Power of Attorney

Accompanying Application Parts:

- Do not publish this application. Nonpublication Request is attached.
- Assignment and Assignment Recordation Cover Sheet
- 37 CFR 3.73(b) Statement by Assignee
- Information Disclosure Statement with Form PTO-1449
 - Copies of IDS Citations
- Preliminary Amendment
- Return Receipt Postcard
- Petition for Application by Other Than All the Inventors
- Declaration of Facts in Support of Applying on Behalf of Omitted Inventors
 - Exhibit A: May 9, 2003 letter
 - Exhibit B: May 19, 2003 letter
 - Exhibit C: Employee Invention and Confidential Information Agreement

3

Petition fee of \$130.00 (37 CFR 1.17(h))

Claim For Foreign Priority

Priority of Application No. filed on
is claimed under 35 U.S.C. § 119.

The certified copy is enclosed.
 The certified copy has been filed in prior application U.S. Application No.
 The certified copy will follow.

Fee Calculation (37 CFR § 1.16)

Applicant is entitled to Small Entity Status under 37 C.F.R. §1.27.

	(Col. 1) Total Claims		(Col. 2) Claims	(Col. 3) Present Extra	Rate	Additional Fee
TOTAL	26	MINUS	20	= 06	x 18 =	\$108.00
INDEP.	02	MINUS	3	= 0	x 84 =	\$-0-
	[] First presentation of multiple dependent claim				\$280 =	
			Basic Filing Fee under 37 C.F.R. §1.16(a)		\$770 =	\$770.00
					TOTAL	\$878.00
			SMALL ENTITY 50% FILING FEE REDUCTION (if applicable)			

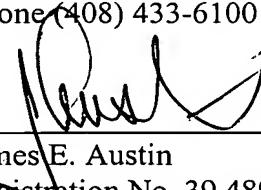
The Commissioner is authorized to charge \$1,088.00 (filing fee, extra claims, two Assignment Recordation fees and Petition fee), or to credit any overpayment, to Deposit Account No. 12-2252 (Order No. 02-6352).

General Authorization for Petition for Extension of Time (37 CFR §1.136)

Applicants hereby make and generally authorize any Petitions for Extensions of Time as may be needed for any subsequent filings. The Commissioner is also authorized to charge any extension fees under 37 CFR §1.17 as may be needed to Deposit Account No. 12-2252 (Order No. 02-6352).

Please send correspondence to the following address:

Customer Number 24319
LSI Logic Corporation
1551 McCarthy Boulevard
Milpitas, California 95035
Telephone (408) 433-6100



James E. Austin
Registration No. 39,489

Date: October 31, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Mirabedini et al.

Attorney Docket No.:
02-6352/LSI1P220

Application No.: Not yet assigned

Examiner: Not yet assigned

Filed: Herewith

Group: Not yet assigned

Title: CALCIUM DOPED POLYSILICON
GATE ELECTRODES**CERTIFICATE OF EXPRESS MAILING**

I hereby certify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service on October 31, 2003 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number **EV332824069US**, addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

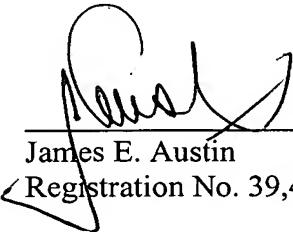


Ryan Eachus**NONPUBLICATION REQUEST AND CERTIFICATION
(35 U.S.C. 122(b)(2)(B)(i))**

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen (18) months after filing. I hereby request that the attached application **NOT** be published under 35 U.S.C. 122(b).

Date: October 31, 2003

James E. Austin
Registration No. 39,489